

WHAT IS CLAIMED IS:

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1. A semiconductor integrated circuit apparatus that is adapted to input/output high frequency signals of a plurality of channels, the apparatus comprising:

10 a plurality of first external terminals that are adapted to input/output the high frequency signals of the corresponding channels;

a second external terminal that has a higher impedance than the first external terminals and is implemented between the first external terminals; and

15 a capacitor unit of which one end is connected to the second external terminal and the other end is arranged to have a predetermined electrical potential.

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2. The semiconductor integrated circuit apparatus as claimed in claim 1, wherein:

25 the second external terminal corresponds to at least one of an external terminal for D video terminal connection verification and an external terminal for D video terminal determination.

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3. The semiconductor integrated circuit apparatus as claimed in claim 1, wherein:

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the second external terminal corresponds to at least one of an external terminal for S video terminal connection verification and an external terminal for S video terminal determination.

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4. The semiconductor integrated circuit
10 apparatus as claimed in claim 1, wherein:
the high frequency signals correspond to video signals.